

FIG.1 (a)

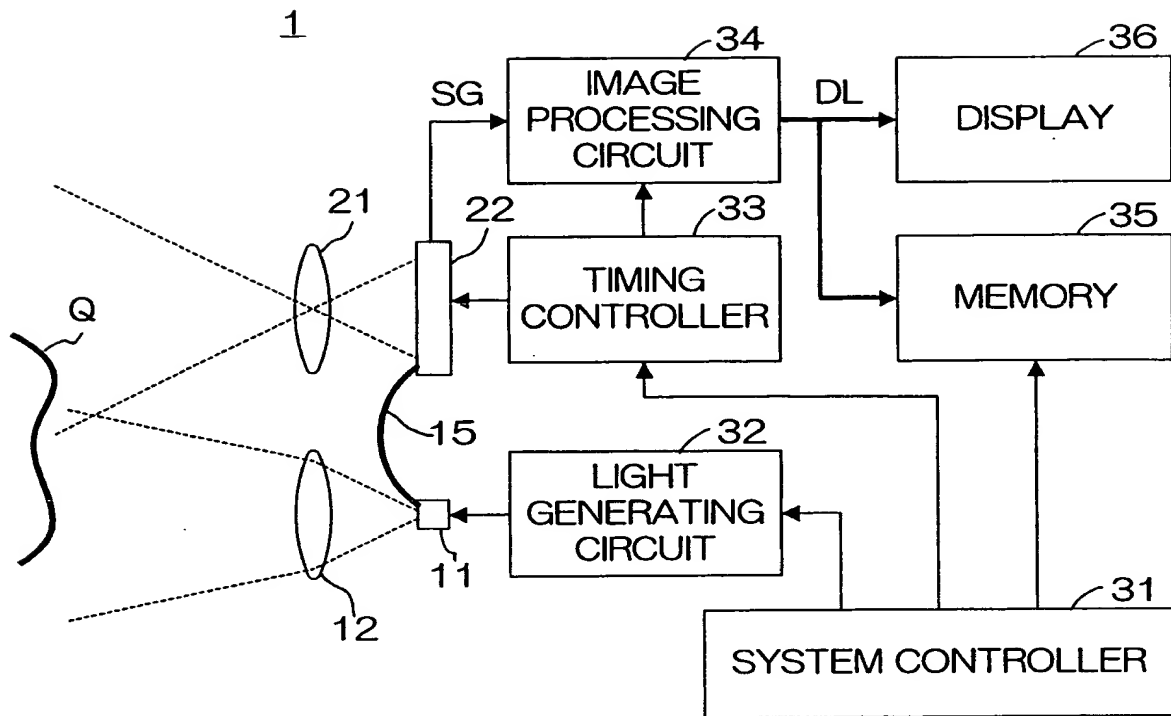


FIG.1 (b)

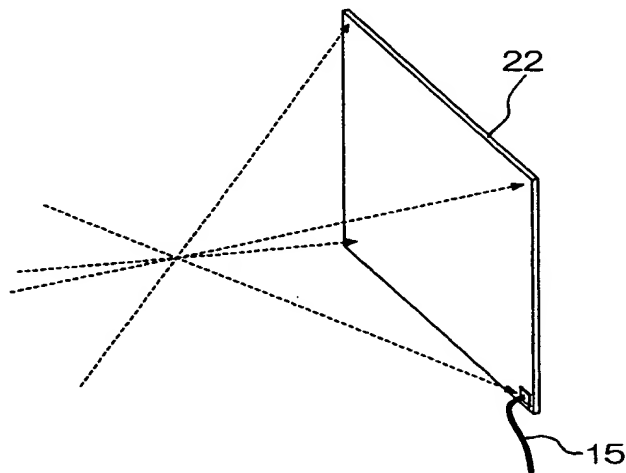


FIG.2

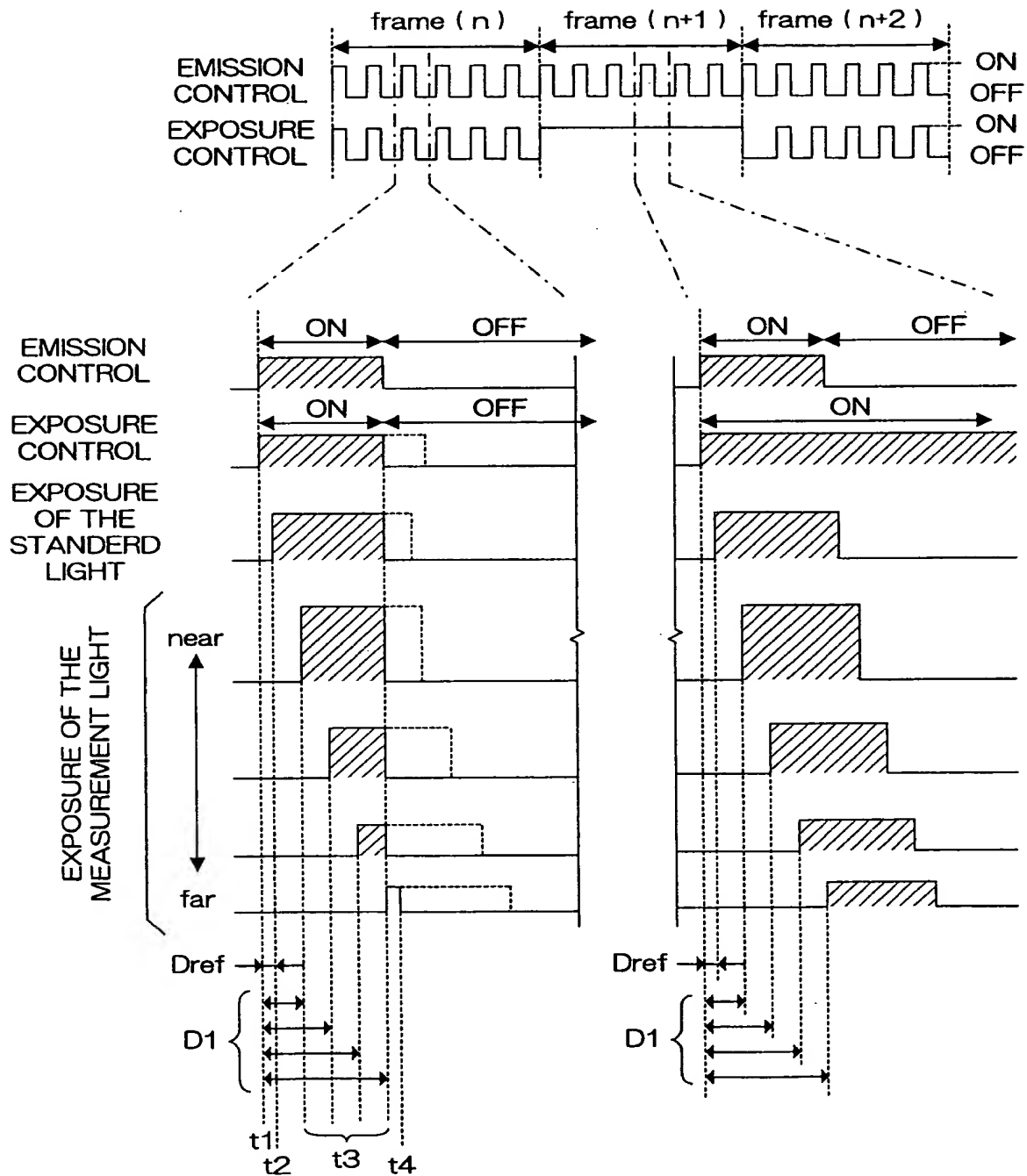


FIG.3

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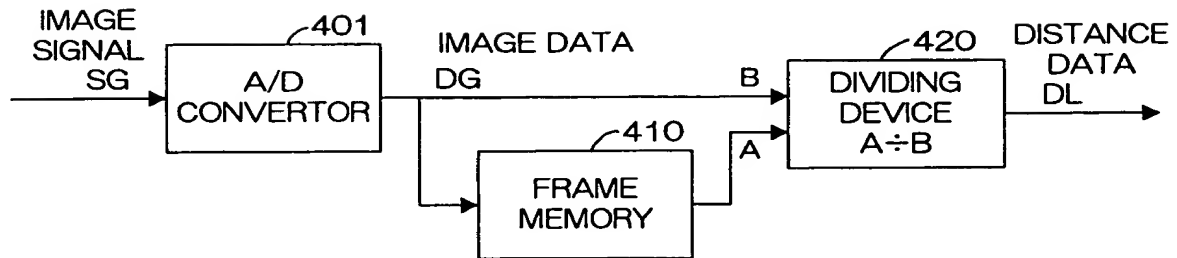


FIG.4(a)

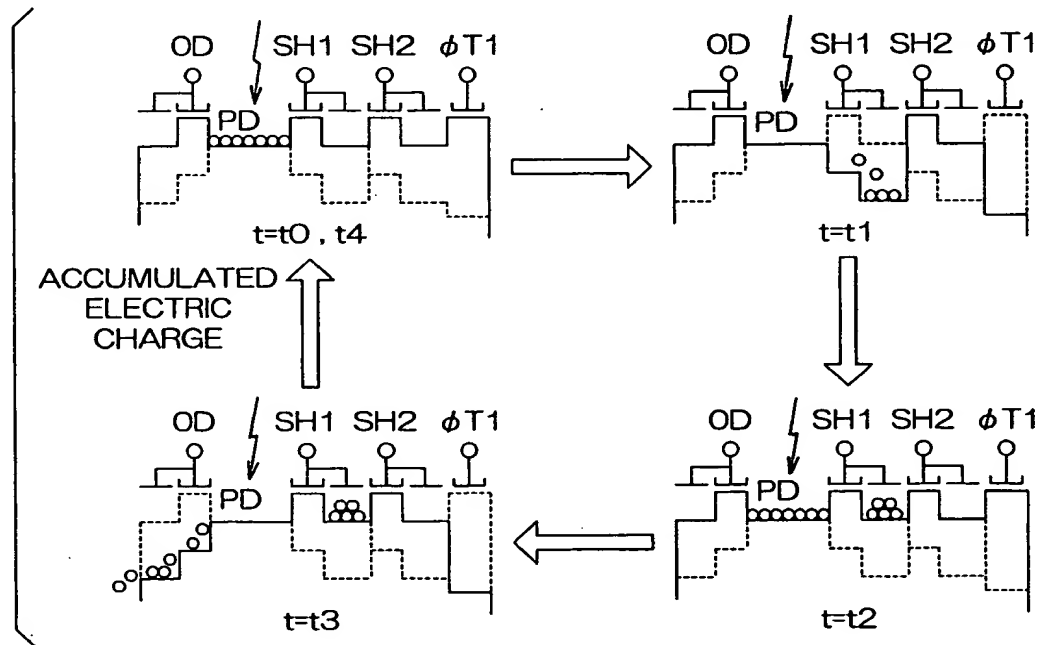


FIG.4(b)

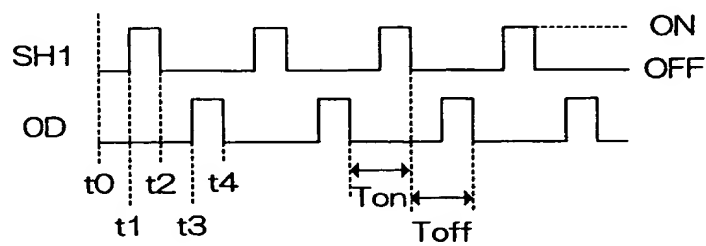
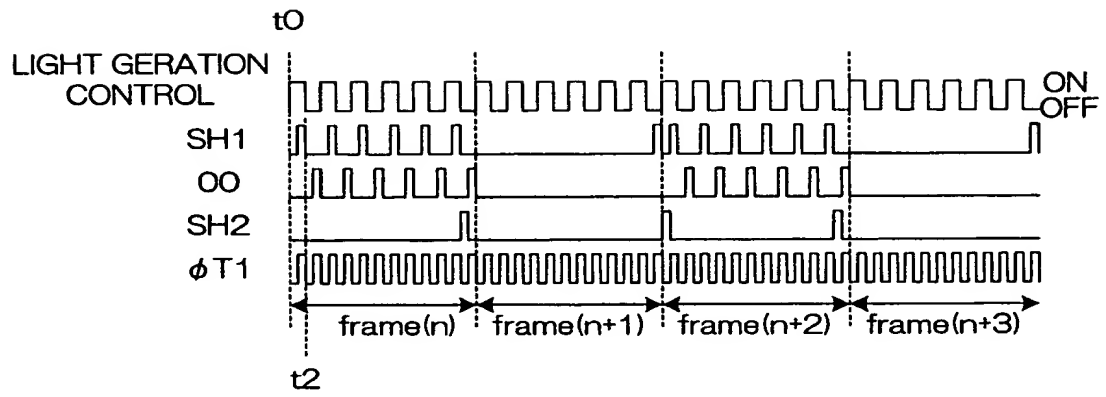


FIG.5



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FIG.6(a)

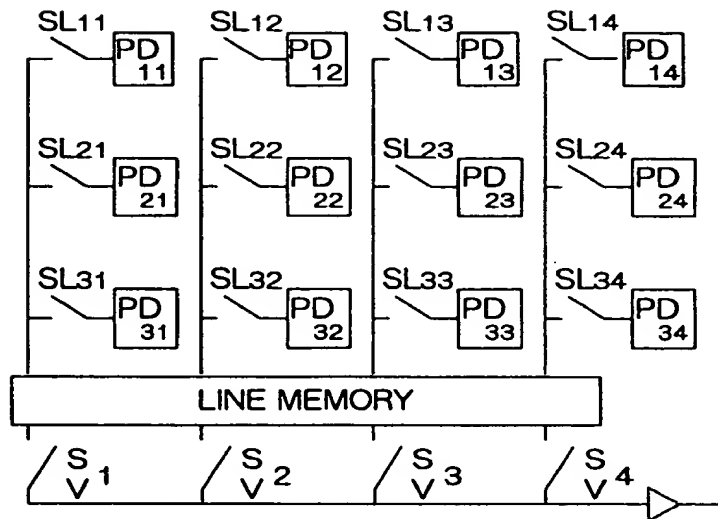


FIG.6(b)

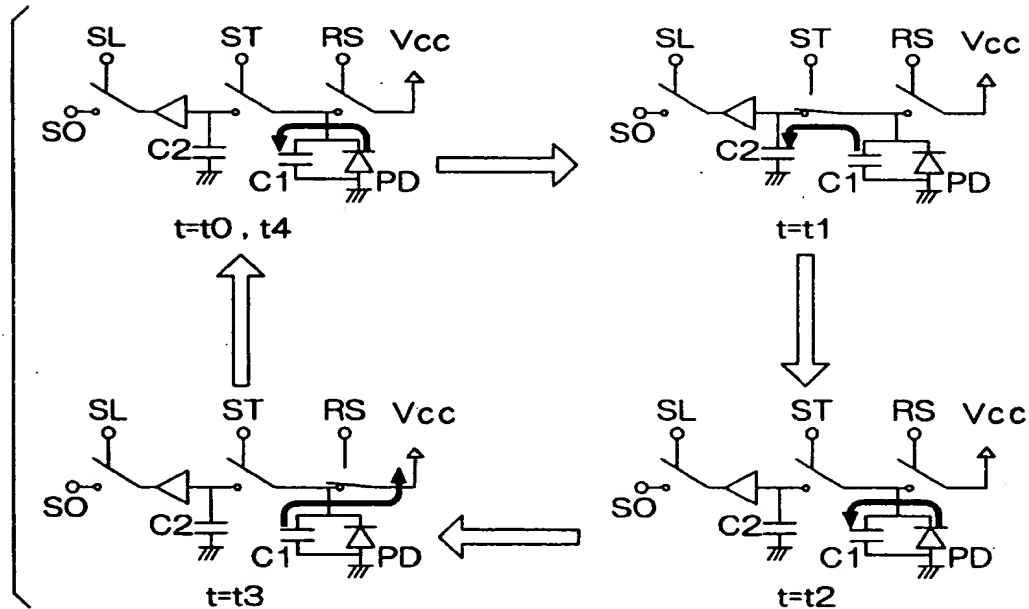


FIG.6(c)

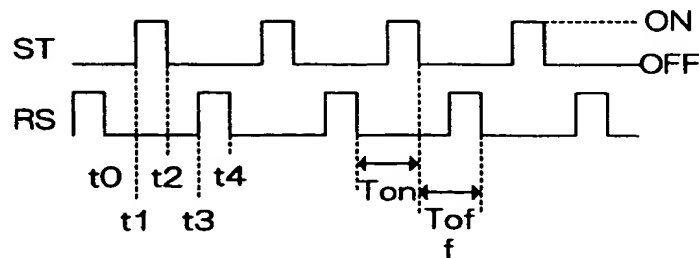


FIG.7

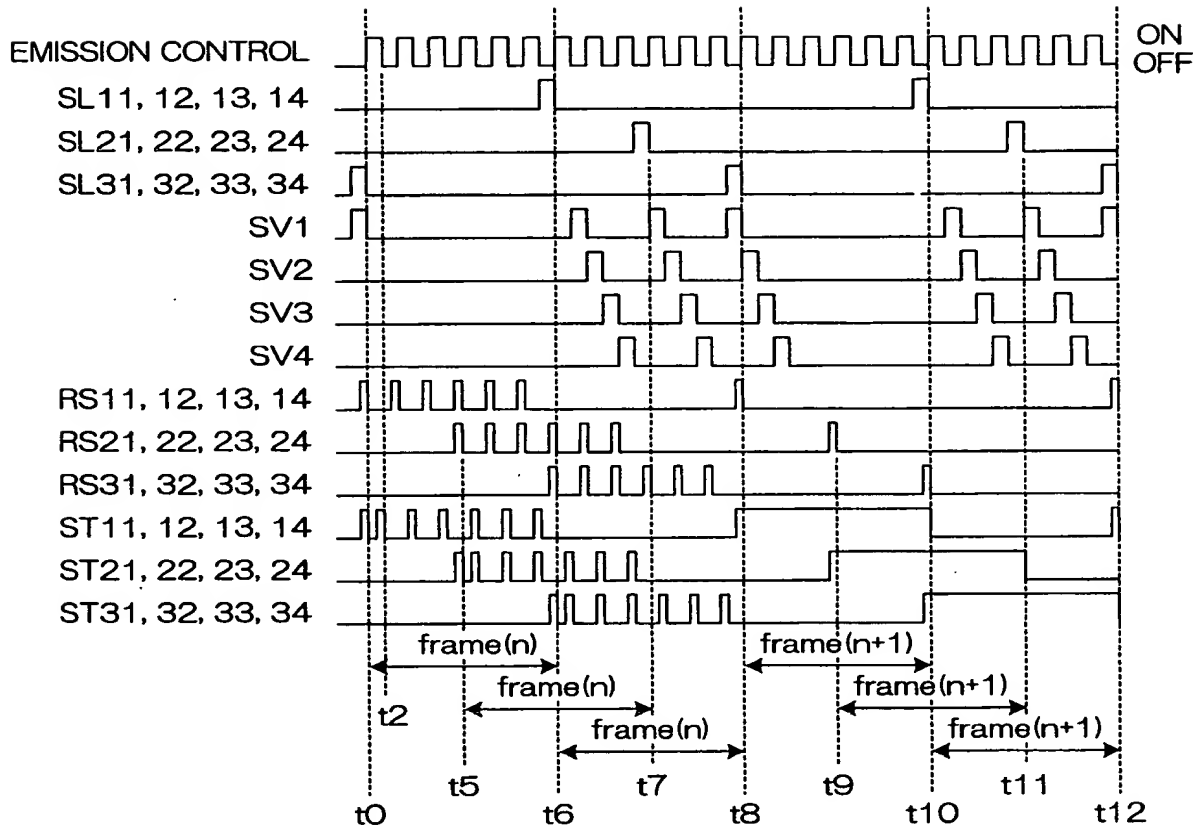


FIG.8

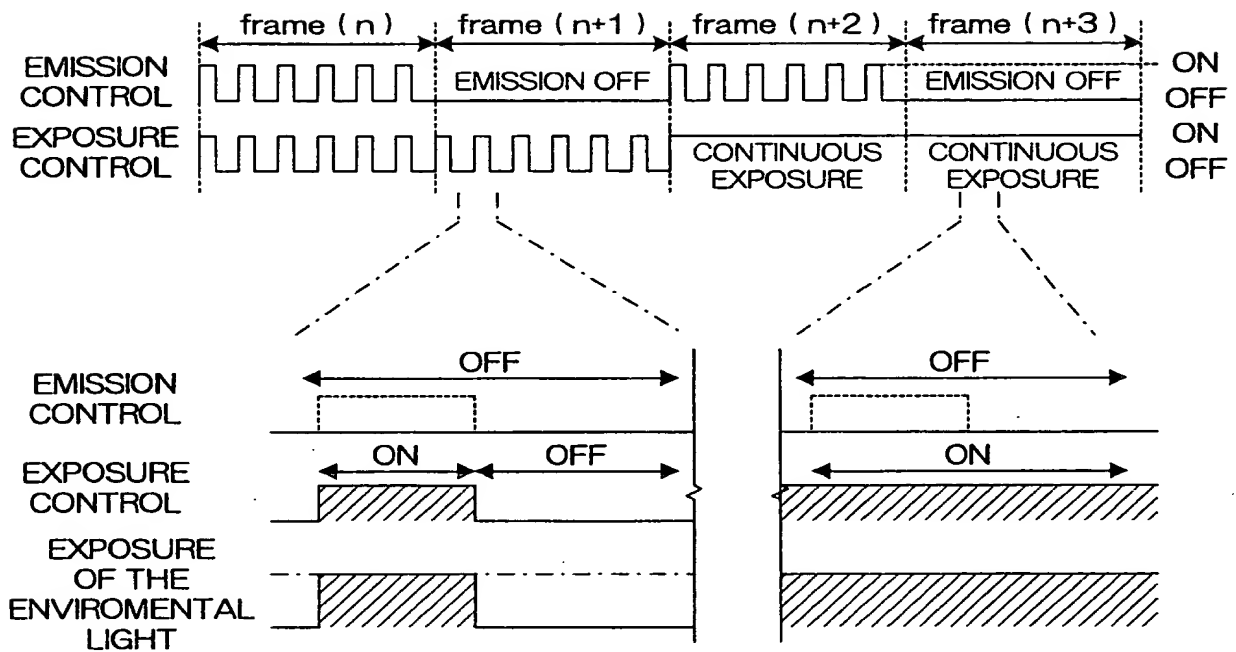


FIG.9

34b IMAGE PROCESSING CIRCUIT

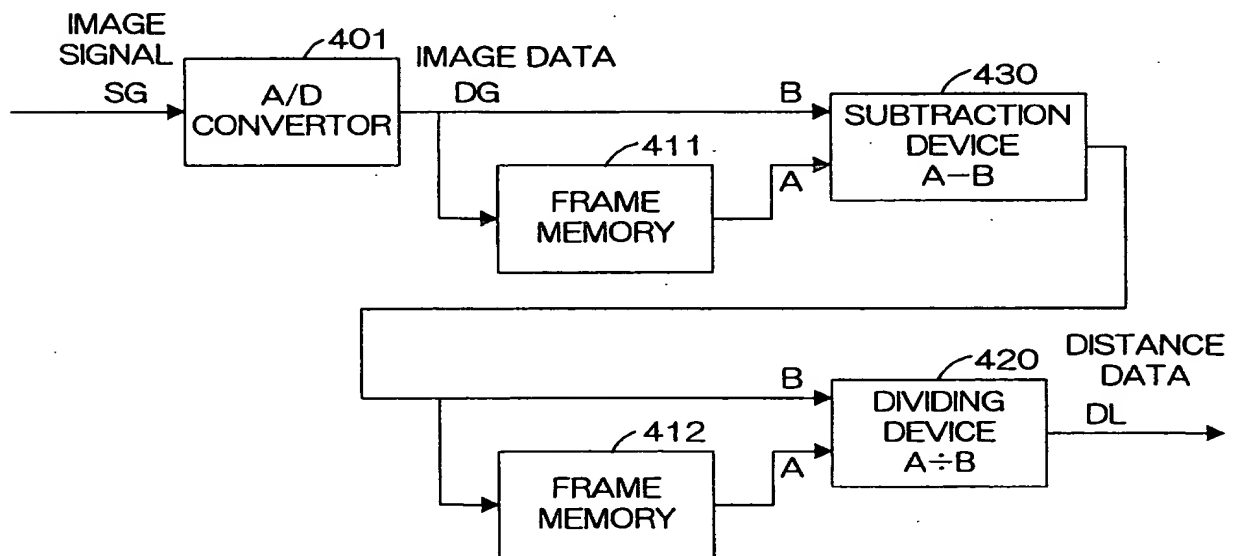


Fig.10(a)

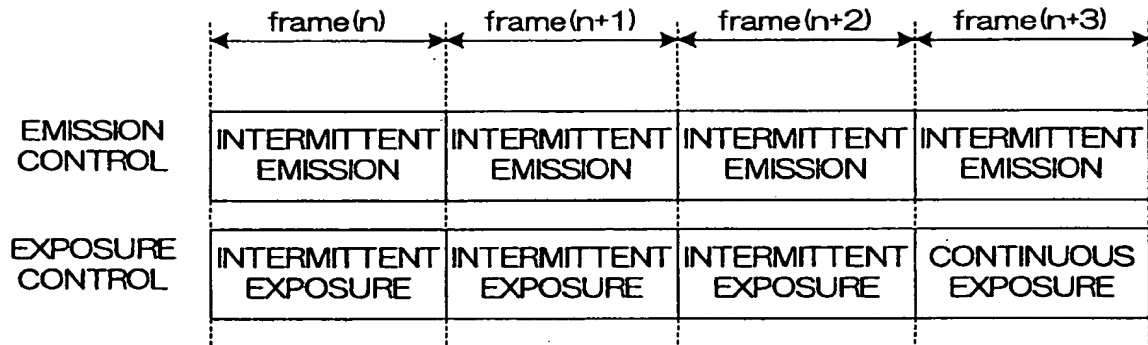


Fig.10(b)

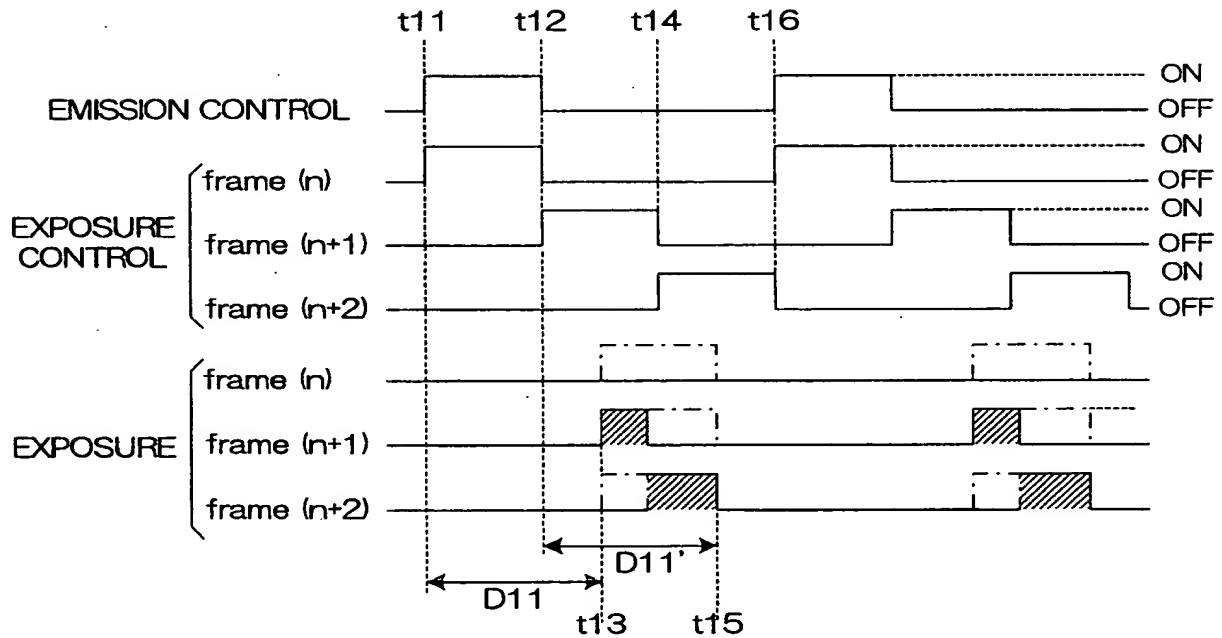




FIG.11

34c IMAGE PROCESSING CIRCUIT

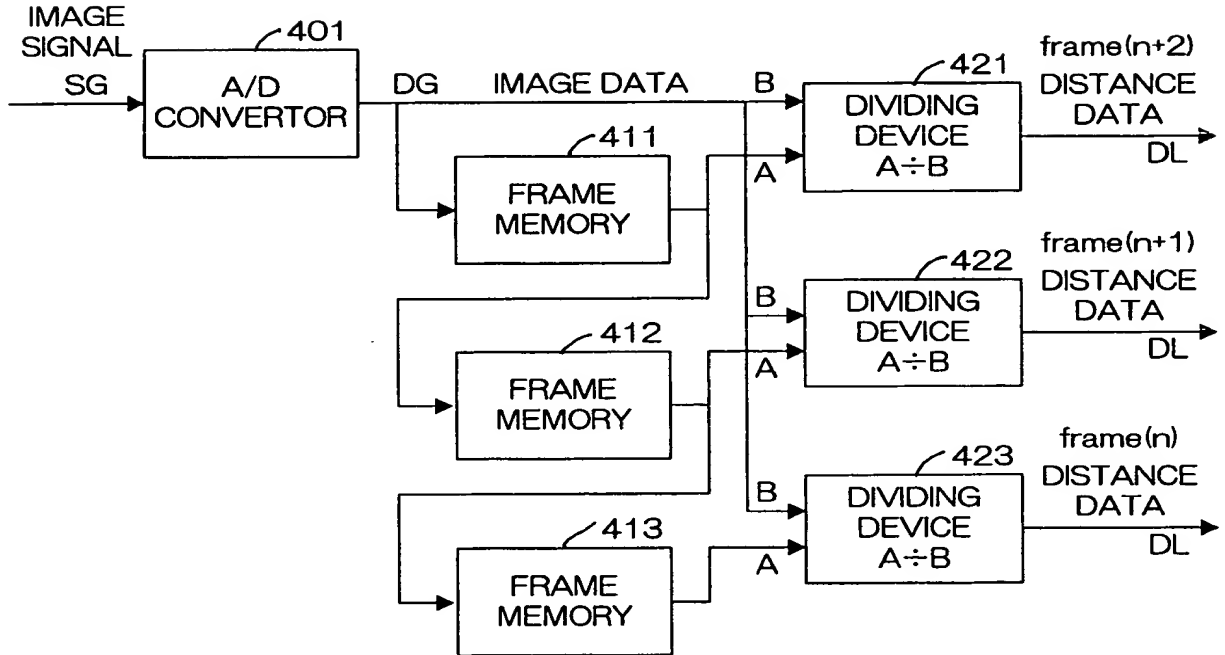


FIG.12

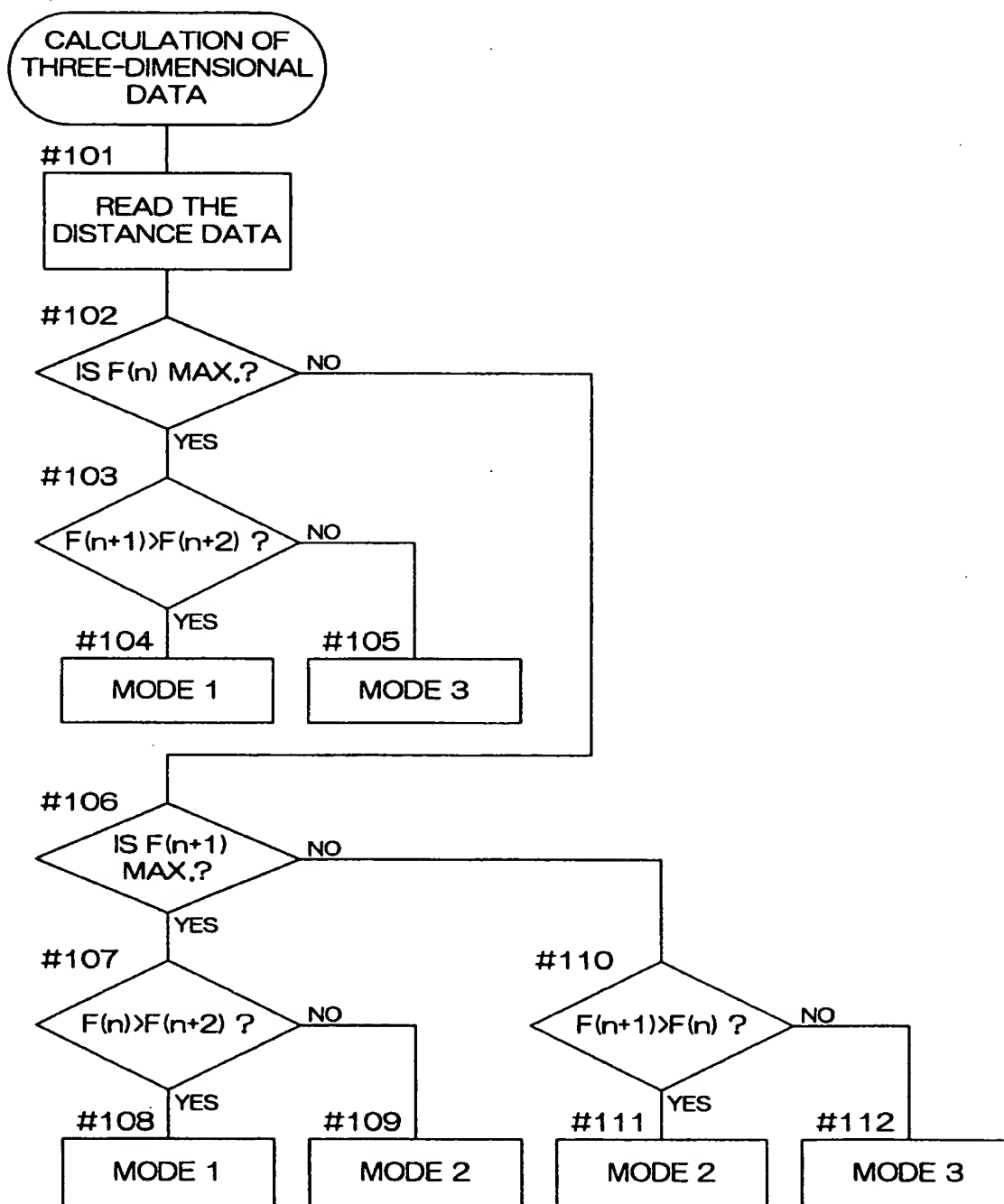


FIG.13

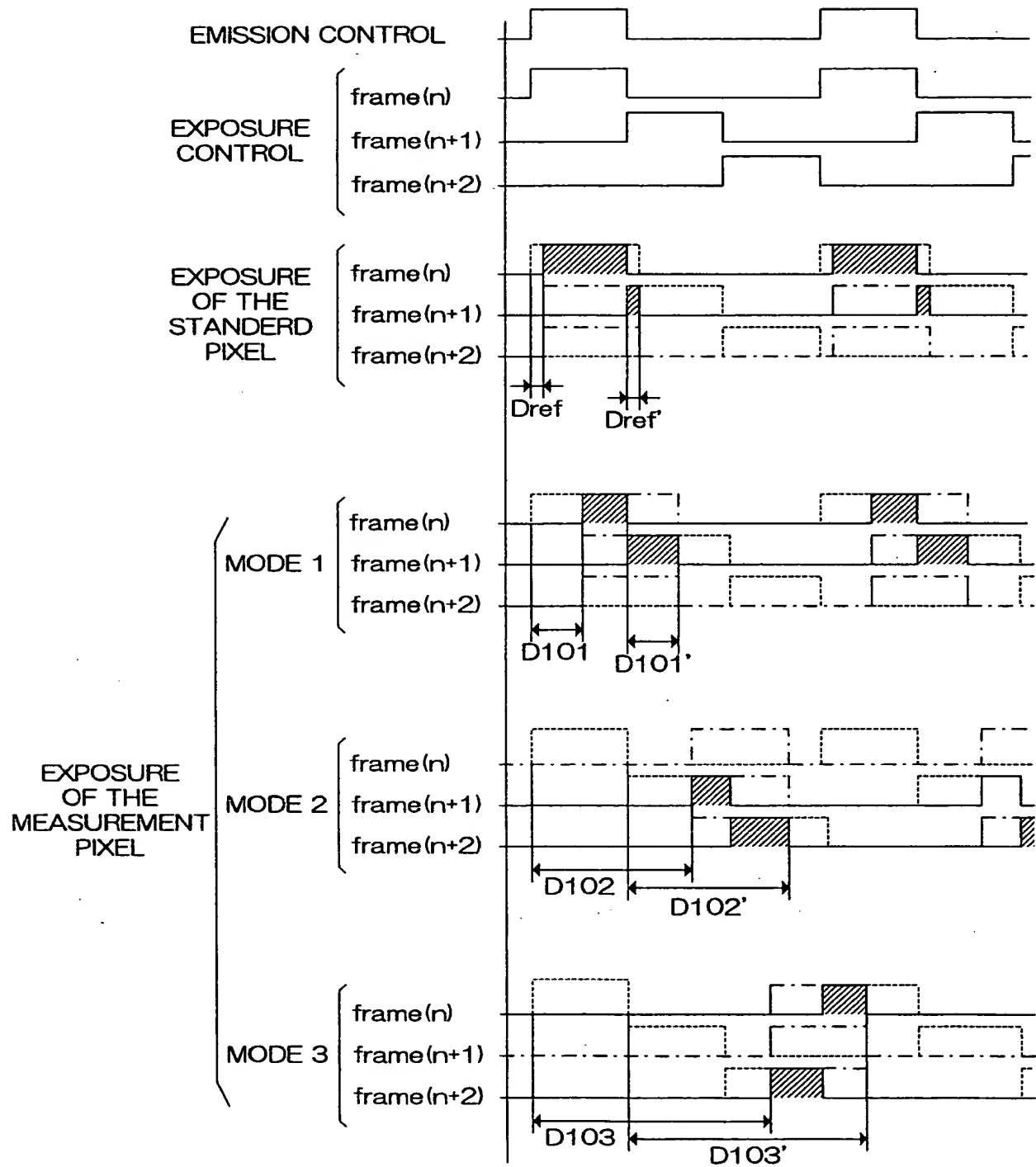


FIG.14(a)

	frame (n)	frame (n+1)	frame (n+2)	frame (n+3)	frame (n+4)	frame (n+5)	frame (n+6)
EMISSION CONTROL	INTER- MITTENT	INTER- MITTENT	INTER- MITTENT	INTER- MITTENT	INTER- MITTENT	INTER- MITTENT	INTER- MITTENT
EXPOSURE CONTROL	INTER- MITTENT	INTER- MITTENT	INTER- MITTENT	INTER- MITTENT	INTER- MITTENT	INTER- MITTENT	CONTIN- UOUS

FIG.14(b)

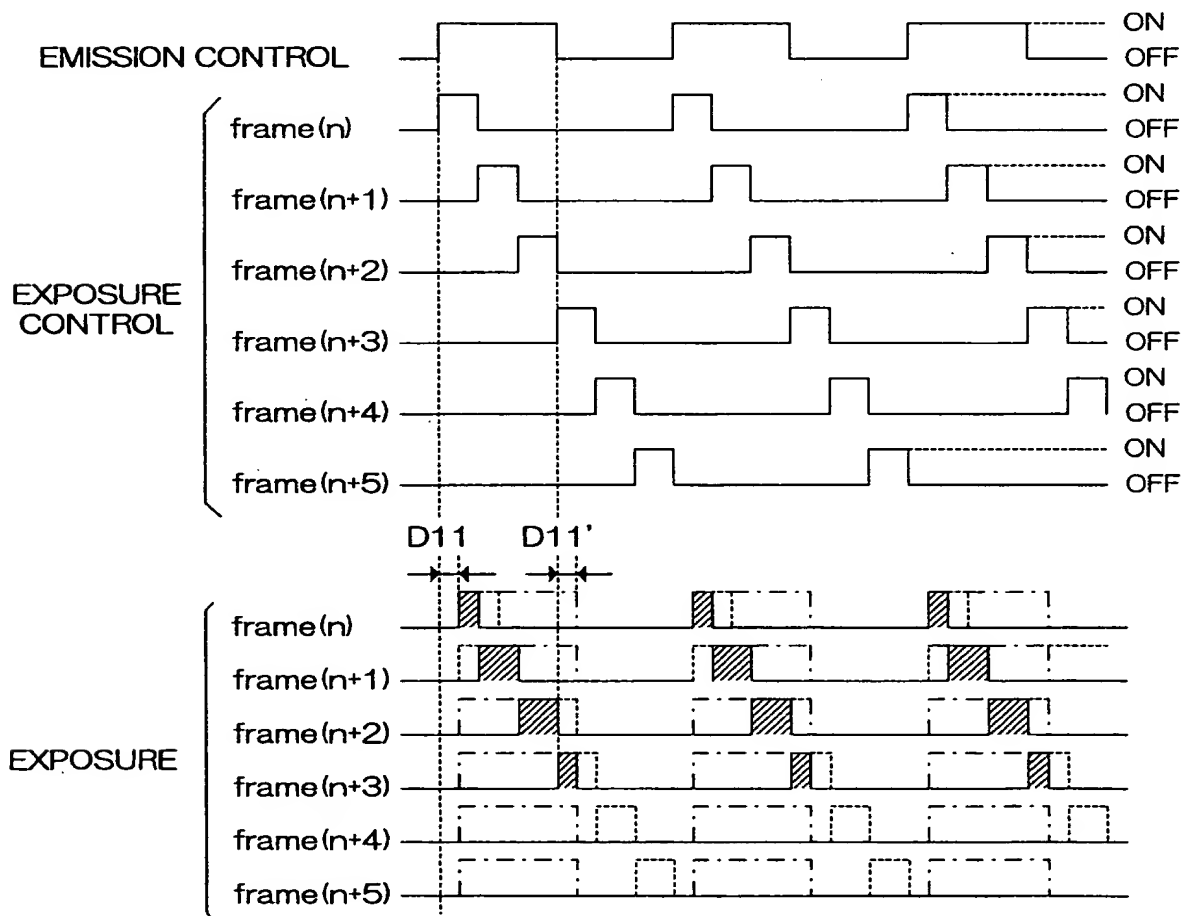


FIG.15

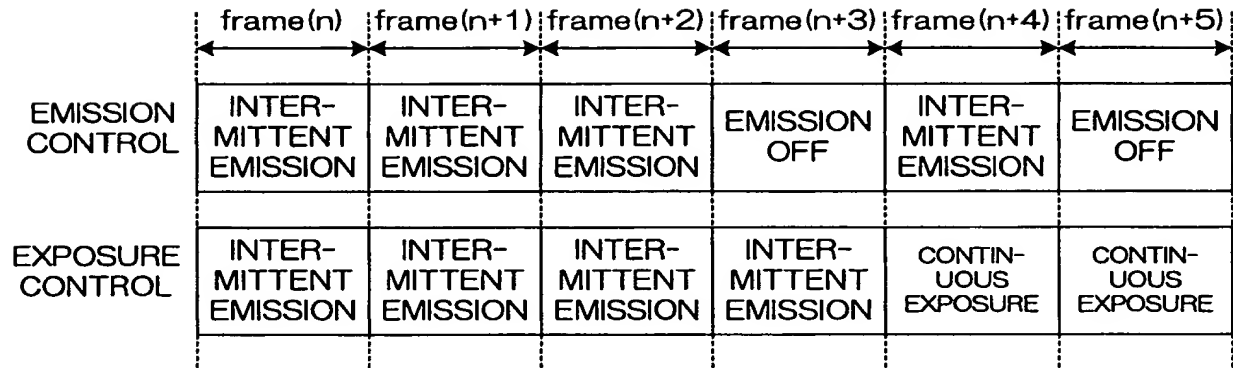


FIG.16

34d IMAGE PROCESSING CIRCUIT

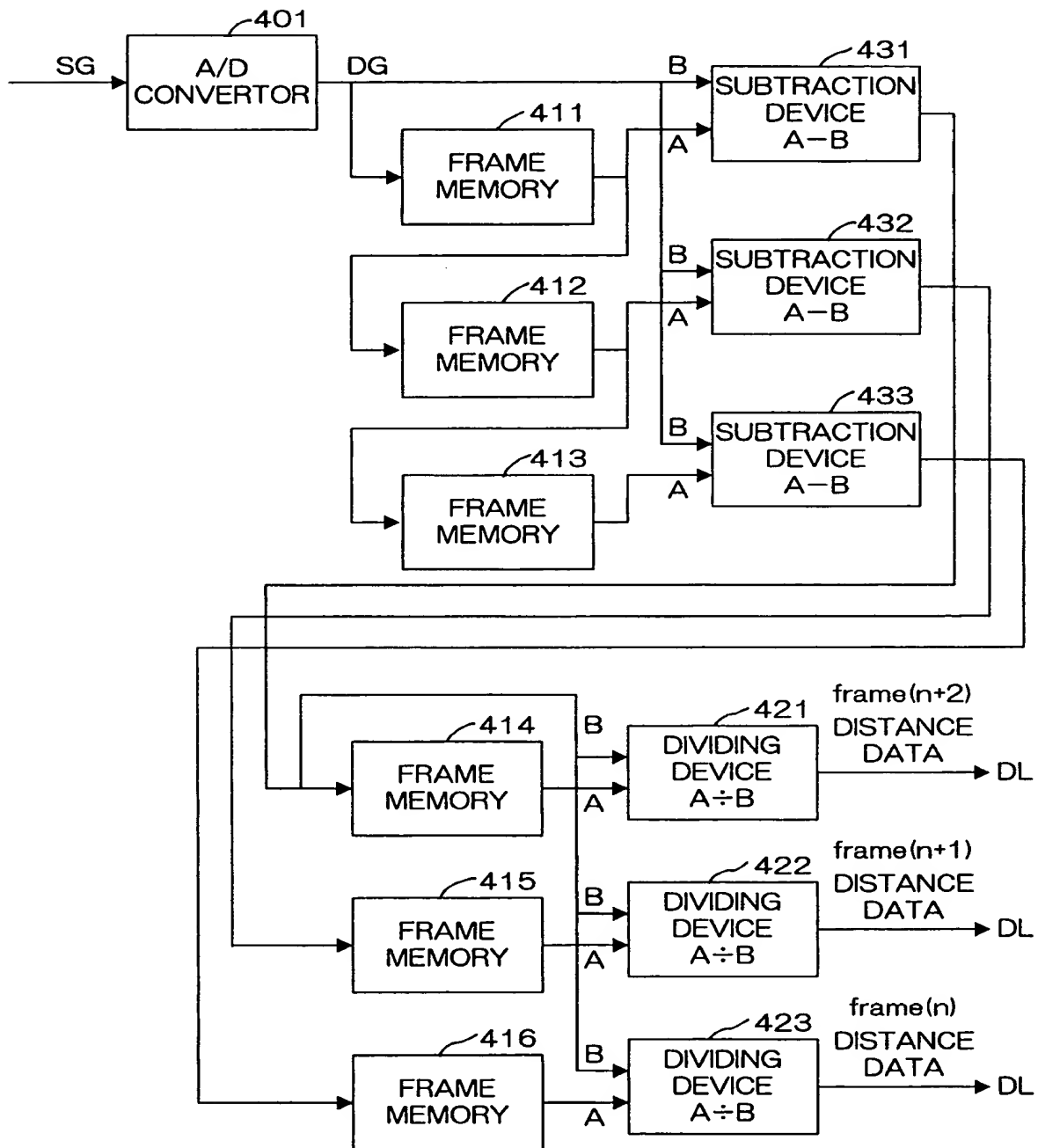


FIG.17(a)

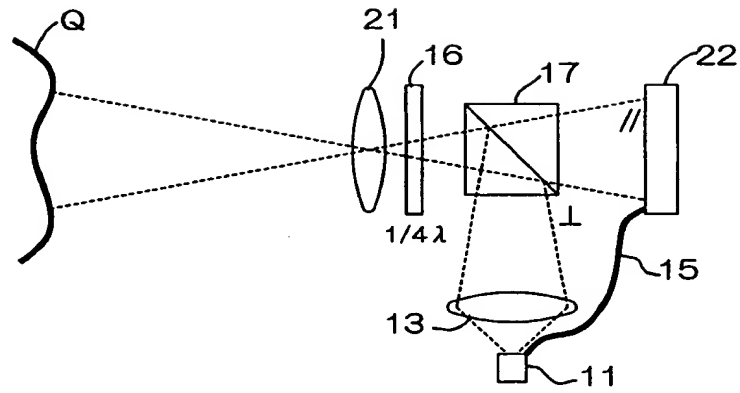


FIG.17(b)

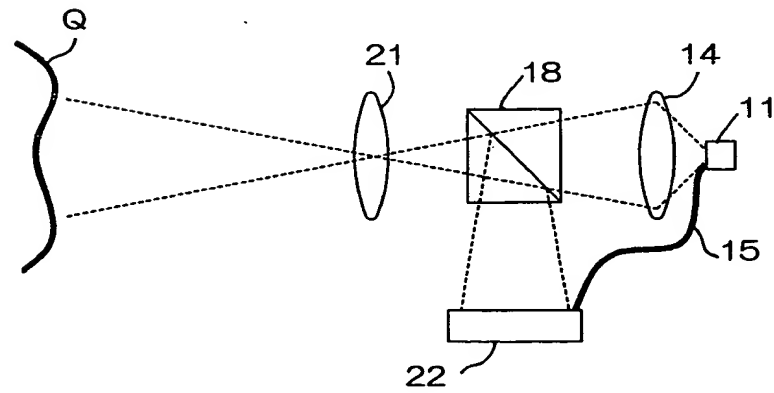


FIG.18

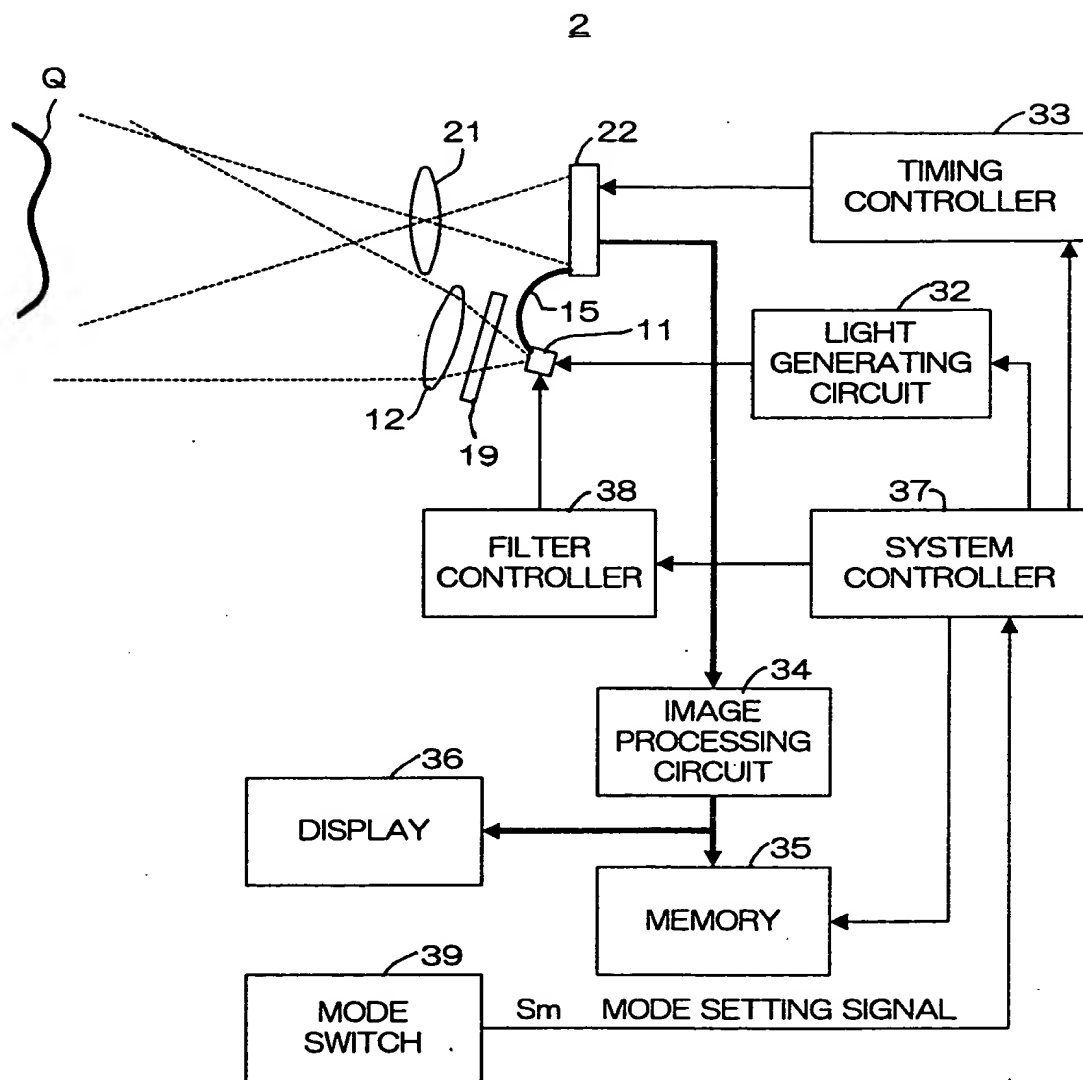




FIG.19(a)

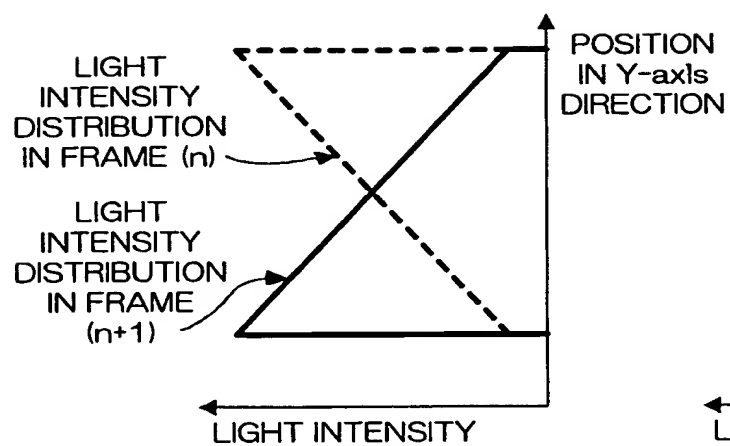


FIG.19(b)

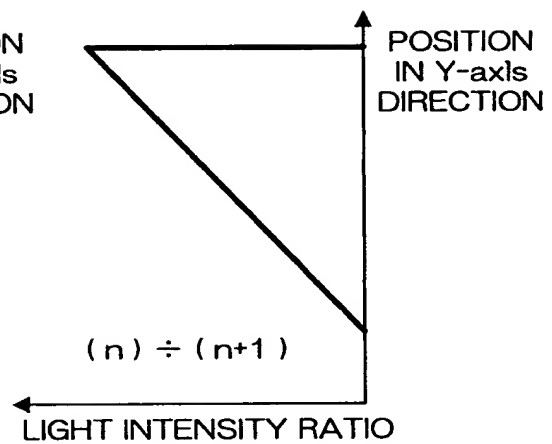


FIG.19(c)

